



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 www.uspto.gov

**\*BIBDATASHEET\***

CONFIRMATION NO. 2061

Bib Data Sheet

<b>SERIAL NUMBER</b> 10/714,393	<b>FILING OR 371(c) DATE</b> 11/17/2003 <b>RULE</b>	<b>CLASS</b> 365	<b>GROUP ART UNIT</b> 2824	<b>ATTORNEY DOCKET NO.</b> 57454-990	
<b>APPLICANTS</b> Hideyuki Noda, Hyogo, JAPAN; Kazutami Arimoto, Hyogo, JAPAN; Katsumi Dosaka, Hyogo, JAPAN; Takeshi Fujino, Hyogo, JAPAN;					
<b>** CONTINUING DATA *****</b> This application is a CON of 09/632,333 08/03/2000 PAT 6,649,984					
<b>** FOREIGN APPLICATIONS *****</b> JAPAN 11-243189 (P) 08/30/1999					
<b>IF REQUIRED, FOREIGN FILING LICENSE GRANTED</b> <b>** 02/24/2004</b>					
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged <u>Examiner's Signature</u> <u>Initials</u>		<b>STATE OR COUNTRY</b> JAPAN	<b>SHEETS DRAWING</b> 15	<b>TOTAL CLAIMS</b> 3	<b>INDEPENDENT CLAIMS</b> 1
<b>ADDRESS</b> 20277					
<b>TITLE</b> SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE HAVING LOGIC CIRCUIT AND DYNAMIC RANDOM ACCESS MEMORY ON THE SAME CHIP					
<b>FILING FEE RECEIVED</b> 1200	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue ) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		